College of Engineering, Cherthala

Tender Notice

Sealed competitive tenders are invited for the supply of following items as per details given below:-

Address of the purchasing Officer: Principal, College of Engineering, Pallippuram P.O, Cherthala, Alappuzha Dist. Pin:-688541, Phone. 0478-2553416, website-www.cectl.ac.in

Sl No	No.& Dated	Items	Estimated Cost	Amount of EMD	Cost of tender form	Sale of tender up to	Receipt of tender up to	Opening of tender
1	P/378/CEC/2009 23-7-2009	Purchase of equipments for communication, project & circuit labs	298000/-	1% of tendered amount	Original: 400+ST Duplicate: 200+ST	25-8-09 2pm	25-8-09 3pm	26-8-09 2pm
2	P/405/CEC/2009 23-7-2009	Purchase of cash safe	82,000/-	1% of tendered amount	Original: 400+ST Duplicate: 200+ST	25-8-09 2pm	25-8-09 3pm	26-8-09 2pm

Tender form and other details can be had from the office of the under signed on all working days. In no way the postal charges for sending tender forms will be met by the institution.

PRINCIPAL

To

Press Notification
Notice Board

Tender No.P/378/CEC/2009 dated: 23-07-2009
Purchase of equipments for communication, project & circuits labs

Sl . No.	Item	Specifications	Quantity Required (Nos.)
1	**DSP Trainer kit-Fixed Point DSP**	1.CPU:TMS 320C641X 2.C6416DSP Development Board with 512K Flash &16MB SDRAM 3.C6416 DSP code composer studio TM V3.01 or latest IDE including the fast simulator and access to Analysis Toolkit on update adviser: 4.Embedded JTAG support via USB 5.High-Quality 24 bit stereo codes 6.Separate audio microphone, line in, loud speaker and line out. 7.Expansion connection plug-in modules 8.On-board standard IEEE JTAG interface 9. Power supply 10.USB cable 11.Technical reference(Both hard copy& soft copy)	**2**
2	**DSP Trainer kit-Floating Point DSP**	1.CPU TMS 320C6713 2. C6713DSP Development Board with 512 k flash and 8 MB SDRAM 3.C6713DSK code composer studio ™V3.01 or latest IDE including the fast simulators and access to Analysis toolkit on update Adviser: 4.Embedded JTAG support via USB 5.High-quality 24 Bit stereo codec 6.Separate audio micro phone, line in, loud speaker and line out. 7.Expansion connector plug-in modules 8.On-board standard IEEE JTAG Interface 9. Power supply 10.USB Cable 11.Technical reference (both hard copy and soft copy)	**2**
3.	**DC Power supply (Variable)**	1. DC output :Min. Range 0-30V, 2A . Should have coarse and fine controls. 2. Current Limit: 100 mA – 2A continuously 3. Setting Resolutions : Voltage : 10mV, Current : 5mA 4. Internal Resistance : ≤10m 5. Stability : ≤ 2.5mV at 30V/2A 6. Recovery Time : ≤ 50 Ω 7. Load Regulation : ≤ (0.05%+10mV) 8. Line Regulation : ≤ (0.05% + 5mV) 9. Temp. Co efficient : ≤ (0.05 +5mV/C) 10. Ripple & Noisy : ≤ 1mVrms 11. Display : 3 digit 7 segment LED indicator for voltage and current 12. Accuracy : ± (1% + 1 digit) 13. Over Range Indication : LED indicator for Overload 14. Built-in overheat, over voltage, overload and short circuit protection 15.Should have proper insulation 16.Power Supply : 220V ± 10% 50HzHz 17.Operation Manual 18. Warranty: Not less than 24 Months	**5**

4.	**Multi Output DC Power Supply**	1. DC Output : * 0-30V, 1A continuously variable by means of coarse & fine controls. * 5V, 1A Adjustable from 4-6V. * 0-+/-15V, 1A Adjustable by means of Coarse & fine controls. 2. Current Limit: 20mA-1A continuously adjustable. 3. Resolution : Voltage :10mV, Current 2mA 4. Display: 3 digits for voltage and 31/2 for current with LED indication for voltage and Current.	**2**
5.	**Microprocessor Trainer Kit**	8085 CPU operating at 6.144 MHz, 8 K bytes of EPROM Monitor, 8 K bytes of RAM with BATTERY Backup, On-board memory expansion upto 64 KB, Three Ch. TIMER/COUNTER using 8253, 48 I/O lines using 2 nos. of 8255, RS232 C interface through SID/SOD lines, Two mode of commands: - ASCII Key pad Mode, - Serial Mode, 20 × 2 LCD Display & 101 ASCII Keyboard, All address, data & control lines are available on 50 pin FRC, Facility for Downloading/Uploading files from/to PC, Accessories: Manuals.	**3**
6.	**Digital Lab Trainer Kit**	Min .10 logic input switches providing high/low output. Min. 10 logic output indicators (LED lamp monitors) with lamp drivers. 4 seven segment LED display with decoder drivers and provision for BCD input. Fixed clock generator outputs of 1Hz,10 Hz,100Hz,1KHz,10KHz and 1MHz. Manual control of pulse using a push button switch (Monopulser). Solderless bread board with 1680 tie points. DC Regulated power supplies +5V,1A and +/- 12V,500mA. TTL/CMOS selectable by a toggle S/W . Input supply 230V +/- 10%,50 Hz ac. Input signals are taken from /output signals are given to spring type nickel plated /SIL sockets. Logic probe facility HI/LO lamp glows according to the level of input signal HI/LO. Accessories: Manual etc.	**10**
7.	**Ethernet Switch**	Standards: IEEE 802.3 10BASE-T IEEE 802.3u 100BASE-TX IEEE 802.3ab 1000BASE-T IEEE 802.3x Full Duplex Flow Control. 1000Base T Ports: 24, Topology: Star. Port: 24 x 10/100/1000Mbps Auto-MDI/MDI-X port.Connector: RJ-45 connector Speed: Ethernet: 10Mbps (Half-duplex), 20Mbps (Full-duplex). Fast Ethernet: 100Mbps (Half-duplex), 200Mbps (Full-duplex) Gigabit Ethernet: 2000Mbps (Full-duplex) Network Media: Ethernet: 2-pair UTP Cat. 3,4,5, up to 10Mbps Fast Ethernet: 2-pair UTP Cat. 5, up to 100Mbps Gigabit Ethernet: 4-pair UTP Cat. 5 plus, up to 1000Mbps, Recommend Cat 5E for Gigabit Operation Protocol: CSMA/CD Diagnostic LEDs: Per Unit: Power Per Port: 3 LEDs for Link/ACT, 1000M , 100M. Power: 100 - 240VAC 50/60Hz, Internal Universal Switching Power	**2**

8.	**Universal IC Tester**	1. It should Test : a. Digital IC's such as 74 Series, 40/45 Series of CMOS IC's. b.Microprocessors such as 8085, 8086, Z80 etc. , Peripheral ICs like 8255, 8279, 8253, 8259 etc., Memory Ics like EPROMs, RAMs c.Analog Ic's such as Opamp, Comparators, Timers, PLL's, VCO, PWM Generator, Opto couplers, Voltages Followers ,ADCs, DACs, Waveform Generators, Voltage Regulators, Transistor Arrays, Analog Switches, Sample and Hold Ics etc. d. Seven segment display of common cathode & common anode type. 2. Should have truth table comparison for Digital Ics functional test for analog Ics. 3.Socket for Ics: Two Nos. of 40 pin DIP ZIF sockets for Digital & Analog IC's. 4.Key Pad: 24 keys Key pad with numerical & functional keys. 5.Display: 16x2 Backlit LCD Display 6.Supply Input Voltage: 230V AC. 7.Accessories: Manual	**1**
9.	**Bread Board**	1680 tie points. Screwed on a hylum base sheet. 2 or 3 Connectors.	**10**

Tender No. P/405/2009/CEC dated: 23-07-2009 purchase of Cash Safe

COLLEGE OF ENGINEERING, CHERTHALA				
SL. NO.	**ITEM**	**SPECIFICATION**		**QTY.**
1	IRON SAFE - 41"	OUT SIDE(mm)	HEIGHT - 1040	ONE
			WIDTH - 635	
			DEPTH - 675	
		INSIDE(mm)	HEIGHT - 840	
			WIDTH - 435	
			DEPTH - 395	
		INTERNAL CUBIC CAPACITY - CUBIC METRE	0.144	
		FITTING : SHELVES - ADJUSTABLE/REMOVABLE ONLY	Two	